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Application Number	NOT YET ASSIGNED
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(Use as many sheets as necessary)

Filing Date	CONCURRENTLY HEREWIT
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First Named Inventor	Vijay Kumar REDDY
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Art Unit	UNKNOWN
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Examiner Name	UNKNOWN
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Sheet	1	of	1	Attorney Docket Number	TI-37048 (1962-10800)
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Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
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	AA	CHEN, LI; BAI, XIAOLIANG; and DEY, SUJIT, Testing For Interconnect Crosstalk Defects Using On-Chip Embedded Processor Cores, Journal Of Electronic Testing: Theory And Applications, 2002, pgs. 529-538. Kluwer Academic Publishers, Netherlands.
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A hand-drawn graph on a grid. The grid has 6 vertical columns and 10 horizontal rows. A curve is drawn starting from the top left, passing through approximately (1, 9), (2, 7), (3, 5), (4, 3), (5, 1), and (6, 0). The word "None" is written at the bottom right of the graph.

Examiner Signature	<i>Jeff Nataloni</i>
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Date
Considered

7/7/05

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). **2** Applicant is to place a check mark here if English language Translation is attached.

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